



Dispozitive nanoelectronice bazate pe grafena

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IMT Bucuresti



Research Centre for Integrated Systems Nanotechnologies and Carbon Based Nanomaterials

CENASIC



Development of a research center within IMT-Bucharest, dedicated to technologies based on carbon nanomaterials: SiC, graphene, nanocrystalline diamond

PE CVD



Nanofab1000 Agile

Graphene Growth

Carbon Nanotubes (CNT, single and multiwall) Growth

Si Nanowire Growth

PECVD of Si, SiN, SiO, SiC at low and high temperatures

<u>a- Si, µc - Si, polySi, SiGe</u>

PECVD and CVD growth using the flexible vapour delivery system (FVD)

ALD



OpAL

400 °C Aluminium substrate holder with PID control, 240 mm diameter

PC Control with OPT software under Windows 7

ICP 65 remote plasma source for radical assisted ALD

up to 5 liquid or solid precursors (incl H2O), bubbled or vapour draw

up to 10 gaseous precursors, with MFC and fast ALD valve controlled delivery lines

RF sputtering



Plasmalab System 400

Circular magnetrons with optional sizes to 200mm round RF/DC/Pulsed DC sputtering (Switched)

RF bias to substrate table

Heated substrate table to 300°C

Load lock

Static, rotation or oscillation modes for deposition

Up to 4 gas channels

Cryo pump option

IMT Bucharest - processing facilities (Furnaces, RTP, CVD, PECVD, PVD, DWL, RIE, DRIE, wafer bonding, etc.)



IMT Bucharest – characterization facilities

(Ellipsometry, NSOM, WLI, micro Raman and TERS, XRD, SECM, FTIR, wafer probing stations, network analyzer, etc.)





Nanoscale Structuring and Characterization Laboratory



Raith - e_Line - dedicated EBL equipment



NanoInk Nscriptor - DPN



NT-MDT Ntegra Aura - AFM & STM



Tescan Vega LMU II - SEM



FEI Nova NanoSEM630 - FEG-SEM



Agilent G200 - Nanoindenter



Raman spectra of single and double layer graphene







A. Dinescu, M. Purica, R. Gavrila, A. Avram and R. Muller "Influence of Low Energy Electron Beam Irradiation of Graphene Ribbon Based Back Gated Field Effect Transistors", MRS 2012 spring meeting, April 9-13, San Francisco, California



Plasmonic Nanostructure Enhanced Graphene -Based Photodetectors



GEOMETRICAL-INDUCED RECTIFICATION IN TWO-DIMENSIONAL BALLISTIC NANODEVICES





CPW structure for microwave measurements

dout < 30 nm, *din* / *dout* > 3 and *L* < 300 nm

M Dragoman, M Aldrigo, **A Dinescu**, D Dragoman, A Costanzo <u>Towards a terahertz direct receiver based on</u> graphene up to 10 THz. Journal of Applied Physics 115 (4), 044307, (2014).



SEM micrographs of graphene rectifier after RIE





JOURNAL OF APPLIED PHYSICS 115, 044307 (2014)

CrossMark

Towards a terahertz direct receiver based on graphene up to 10 THz

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We present a study for a THz receiver based on graphene. First, the dipole and the bowtie THz antennas on graphene are designed, and followed by the on-wafer fabrication of a graphene diode matched to the antenna. Finally the responsivity of the receiver up to 10 THz is computed. Our results show that the antenna and the diode behaviors exhibit new properties (e.g., the antennas are acting as high reactive impedance surfaces, the diode is rectifying only due to its geometrical shape). These new properties are due to the physical properties of graphene having the carrier transport described by Dirac equation. © 2014 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4863305]



NDR applications: frequency multipliers, mixers, fast switches, high frequency oscillators Classic NDR devices: Esaki and Gunn diodes

NDR in nanostructures:

- X.Q. Deng et al. "Electrode conformation-induced negative differential resistance and rectifying performances in a molecular device," Appl. Phys. Lett. 95, 163109 (2009).

- Y. Ya et al. "Electrical instability and negative differential resistance in single Sb-doped ZnO nanobelts/SiO_x/p-Si heterostructure device," Appl. Phys. Lett. 96, 093107 (2010).

- N.M. Park et al. "Negative differential resistance in silicon quantum dot metal-insulator semiconductor structure," Appl. Phys. Lett. 89, 153117 (2006). -M. Dragoman et al. "Negative differential resitance in GaN nanowire network," Appl. Phys. Lett. 96, 053119 (2010).

NDR in Graphene FETs:

-L. Britnell et al "Resonant tunneling and negative differential conductance in graphene transistors," Nature Communications 4, 1794 (2013).





-G. Liu et al "Graphene-based non-Boolean logic circuits," J. Appl. Phys. 114, 154310 (2013).





M.I. Katsnelson, K.S. Novoselov, A. K. Geim, "Chiral tunnelling and the Klein paradox in graphene," Nature Physics 2, 620 (2006).



APPLIED PHYSICS LETTERS 90, 143111 (2007)

Negative differential resistance of electrons in graphene barrier

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FIG. 2. Transmission of the graphene barrier as a function of the applied voltage at various Fermi wave numbers: $k_F = 0.25 k_{F0}$ (dotted line), $k_F = 0.3 k_{F0}$ (solid line), and $k_F = 0.35 k_{F0}$ (dashed line).



FIG. 4. *I-V* characteristics of graphene barrier at various incident angles: $\varphi_1 = 10^\circ$ (dotted line), $\varphi_1 = 15^\circ$ (solid line), and $\varphi_1 = 20^\circ$ (dashed line).



Negative differential resistance in graphenebased ballistic field-effect transistor with oblique top gate

Mircea Dragoman¹, Adrian Dinescu¹ and Daniela Dragoman²



Global alignment marks patterning (e-beam) -Global alignment marks metallization and lift-off

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-Graphene patterning (1) (e-beam)
-RIE (1)
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-Local alignment marks patterning (e-beam) -Local alignment marks metallization and lift-off

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-Graphene patterning (2) (e-beam)
-RIE (2)
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-S and D patterning (e-beam)
-S and D metallization and lift-off
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-S and D contact pads patterning (e-beam) -S and D contact pads metallization and lift-off

-Gate dielectric deposition and patterning (e-beam)

-Gate patterning (e-beam) -Gate metallization and lift-off

Global alignment marks patterning

Global alignment marks metallization and lift-off



Graphene patterning (1)

RIE (1)



Local alignment marks patterning





Local alignment marks metallization and lift-off





S and D patterning



S and D metallization and lift-off 5nm Ti/30nm Au





Mag = 97.99 K X 100 nm



Pa₁

Pa 1 = 199.8 nm

S and D contact pads patterning



S and D contact pads metallization and lift-off (5nm Ti/ 150nm Au)



Gate dielectric deposition and patterning HSQ - 25nm thickness



Gate patterning



Gate metallization and lift-off 5nm Cr/ 40nm Au



Electrical Measurements



Semiconductor Characterization System - 4200-SCS/C/Keithley with Wafer Probing Station –Easyprobe EP6/ Suss MicroTec





Electrical Measurements



NDR behavior of the graphene FET with oblique gate at VTG = 0.5 V (dashed red line), 1 V (dotted blue line), and 1.5 V (solid red line).
 SUPPA 35-29-87
 Mag = 11.50 KX
 EHT = 10.00 kY
 Signal.

 SLIPPA 35-29-87
 Mag = 11.50 KX
 EHT = 10.00 kY
 Signal.

PVR ratio is tuned by the gate voltage. For a top-gate voltage of 1.5 V, the maximum drain current attains 105 μ A at a drain voltage of 1.6 V and decreases to about 12 μ A at VD = 2 V. The PVR for this top-gate voltage value is about 8.75. For the top-gate voltage of 0.5V maximum and minimum ID values are about 90 μ A and 12 μ A, respectively, the corresponding PVR being 7.5.



Drain current-drain voltage dependences of the graphene FET at various top-gate and back-gate voltages: VTG = -2 V (solid black line), VTG = -1 V (dotted magenta line), VTG = 0 V (dotted red line), VTG = 1 V (dotted blue line), VTG = 2 V (solid blue line), VTG = 2 V and VBG = 40 V (solid magenta line), VTG = 2 V and VBG = 50 V (dotted black line), and VTG = 2 V and VBG = 60 V (solid red line)

Areas with defects - No NDR, but strong and tunable nonlinear I-V dependence



